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PATENT AND TRADEMARK OFFICE									
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Applicant: ASTRID ELBE ET AL.					
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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)									
CD		Drescher, W. et al.: "VLSI Architectures for Multiplication in GF (2 <sup>m</sup> ) for Application Tailored Digital Signal Processors", IEEE, 1996, pp. 55-64							
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Sheet 1 of 1

**Applicant** 

Astrid Elbe et al.

Filing Date
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EXAMINER DATE CONSIDERED 5/13/04

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